

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination CLARKE ET AL.	
		Examiner Vuthe Siek	Art Unit 2825	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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*	B	US-7,047,139	05-2006	Shtrichman, Ofer	702/22
*	C	US-2004/0078674	04-2004	Raimi et al.	714/033
*	D	US-2004/0019468	01-2004	De Moura et al.	703/2
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
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	V	Biere et al., "SAT AND ATPG: BOOLEAN ENGINES FOR FORMAL HARDWARE VERIFICATIOIN," IEEE, FEB 2002, PP. 782-784.
	W	Clarke et al., "BEHAVIORAL CONSISTENCY OF C ADN VERILOG PROGRAMS USING BOUNDED MODEL CHECKING," DAC, JUNE 2003.
	X	Cabodi et al., "CAN BDDs COMPETE WITH SAT SOLVERS ON BOUNDED MODEL CHECKING", DAC, JUNE 2002, PP. 117-122.

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.